		EEE Dept.
	S J P N Trust's	**EESSA**
	Hirasugar Institute of Technology, Nidasoshi.	**Activity Report**
	Inculcating Values, Promoting Prosperity	
	Approved by AICTE, New Delhi, Permanently Affiliated to VTU, Belagavi	**2022-23**
	Recognized under 2(f) & 12B of UGC Act, 1956	**(Even Sem)**
	Accredited at 'A' Grade by NAAC & Programmes Accredited by NBA: CSE & ECE	

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGG.

Date of Activity	**24/07/2023**
Time	**3.00PM to 4.30PM**
Type of Activity **(Co-curricular)**	**Webinar on "PCB Design"**
Resource Person:	**Shri. Shrikant**
Professional Details of Resource **Persons/Guests**	**Project Engineer, Embedded System,** **Pantech E-Learning, Hyderabad.**
Year / Class –	**1ˢᵗ and 2ⁿᵈ Year**
No. of students-	**66**
Activity In charge-	**Prof. H. R. Zinage & Prof. O. B. Heddurshetti**

Description of Activity:

EESSA in association with Pantech E-Learning organized a webinar on **"PCB Design"** on 24th July 2023 at 3:00PM for the faculty and students of Electrical and Electronics Engineering department. Dr. B. V. Madiggond, Professor and Head E&EE department, Convener of the webinar, all the faculty, staff members and students of E&EE dept. joined the webinar through google meet.

Prof. Hemalata R. Zinage, event coordinator welcomed the gathering and introduced the resource person.

The resource person Shri. Shrikant, Project Engineer, Embedded System, Pantech E-Learning discussed about different types of PCB and softwares used for PCB design. Further, He elaborated the use of Proteus software for PCB design and at the end, interacted with the participants.

The webinar concluded with feedback from students and vote of thanks by Prof. Onkar. B. Heddurshetti.

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in

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Activity Photographs: Webinar on "PCB Design"